REMARKS

This Amendment cancels claims 11-13, adds new claims 14-18, rewrites claims 1, 3 and 10, and makes editorial corrections to the specification. The features of new claim 14 are taken from claim 10, while new claims 15 and 16 are taken from claim 11. New claims 17 and 18 are based on claims 12 and 13 and are further supported by page 7, lines 13-26. The changes to claims 1 and 3 are merely editorial. Claims 1-10 and 14-18 are pending.

This Amendment overcomes the objection to the declaration. More particularly, a new declaration which recites the citizenship of each inventor is attached. Reconsideration and withdrawal of the objection to the declaration are earnestly requested.

The objection to claim 10 (now claim 14) is respectfully traversed. More particularly, one of ordinary skill in the art would understand "SSRAM" to mean Synchronous Static Random Access Memory. See "The Spyder System for Rapid Prototyping," http://dd-t.com/spyder.htm; "About SRAM Memory," http://www/memorytesters.com/articles/about_sram.htm; and "TMS320C67x vs. ADSP-21160: Which Floating-Point DSP Offers Highest Performance?," http://www.techonline.com/

community/ed_resource/feature. Reconsideration and withdrawal of the objection to claim 10 are earnestly requested.

This Amendment also overcomes the 35 U.S.C. § 112, second paragraph, rejection of claims 1, 3 and 11. More particularly, claim 11 has been canceled in favor of new claims 15 and 16, while claims 1 and 3 have been rewritten to eliminate ambiguity. Reconsideration and withdrawal of the indefiniteness rejection of claims 1, 3 and 11 are earnestly requested.

The 35 U.S.C. § 102(b) rejection of claims 1-13 over U.S. Patent 5,572,710 to Asano et al. is traversed. The claimed invention is a real time functional replicator of a specific integrated circuit comprised of a processing unit and peripherals in order to perform specific digital and/or analog functions controlled by specific software, the specific integrated circuit being designed to be incorporated into a specified application board; the replicator device comprising:

a processing module that is functionally identical to the processing unit of the specific integrated circuit,

a plurality of peripheral modules each able to implement one or more digital and/or analog functions, each of the functions being able to be selected separately, and

function interconnection means for establishing the connections between the processing module and one or more digital and/or analog functions previously selected and located in at least one of the peripheral modules, the functions being identical to said specific functions of said specific integrated circuit, such that the replicator behaves identically to the specific integrated circuit when the specific software is run.

An important difference between Asano et al. and the present invention is that Asano et al. discloses emulation software which is used to accelerate a hardware component. According to the present invention, the emulation module is integrated in the hardware.

Reconsideration and withdrawal of the anticipation rejection of claims 1-13 over Asano et al. are earnestly requested.

It is believed this application is in condition for allowance. Reconsideration and withdrawal of the objections to the declaration and claim 10, and all rejections of claims 1-13, and issuance of a Notice of Allowance directed to claims 1-10 and 14-18, are earnestly requested.
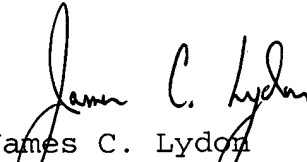
A Petition and fee for a two month Extension of Time is attached. It is not believed any additional fee is required for

entry and consideration of this Amendment.   Nevertheless, the

Commissioner is authorized to charge our Deposit Account No. 50-

1258 in the amount of any such required fee.

                            Respectfully submitted,

                            James C. Lydon
                            James C. Lydon
                            Reg. No. 30,082

Atty. Case No.: **BONN-039**
100 Daingerfield Road
Suite 100
Alexandria, Virginia 22314
Telephone: (703) 838-0445
Facsimile: (703) 838-0447

Enclosures:
        Inventors' Declaration
        Petition for Extension of Time
        "The Spyder System for Rapid Prototyping,"
http://dd-t.com/spyder.htm
        "About SRAM Memory,"
http://www/memorytesters.com/articles/about_sram.htm
        "TMS320C67x vs. ADSP-21160: Which Floating-Point DSP Offers
Highest Performance?,"
http://www.techonline.com/community/ed_resource/feature